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(54) **PIXEL STRUCTURE AND MANUFACTURING METHOD**

(57) Disclosed are a pixel structure and a manufacturing method. The manufacturing method comprises: preparing a substrate (11); manufacturing, on the substrate, a black photoresist layer (12) with an accommodation cavity (121) and an isolation area (122); coating a surface, other than the isolation area, of the black photoresist layer with a polyelectrolyte solution, air-drying

the surface, and forming a polyelectrolyte layer (13); coating a surface of the polyelectrolyte layer with a metal nano-particle solution, air-drying the surface, and forming a metal particle layer (14); and aligning and transferring a micro lightemitting diode (15) to the black photoresist layer. By means of the method, the light utilization rate of a micro light-emitting diode can be increased.

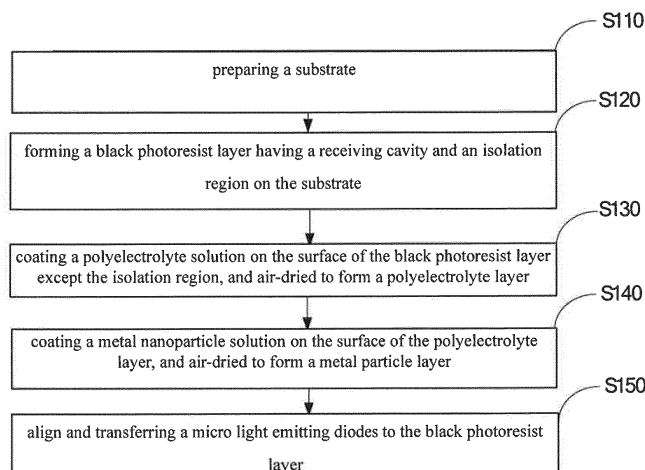


FIG. 1

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Description

FIELD OF THE INVENTION

[0001] The present application relates to a display technology field, and more particularly to a pixel structure and method for the fabrication thereof.

BACKGROUND OF THE INVENTION

[0002] With the rapid development of display technology, the applications of the micro light emitting diode, Micro LED display are also attracting more and more attention by the industry.

[0003] Micro LEDs are self-luminous units driven by current, each of the independent light emitting units is uniformly illuminated in all directions in the space, so that most of the light energy is lost during this process.

SUMMARY OF THE INVENTION

[0004] The present disclosure provides a pixel structure and a method for the fabrication thereof, and can improve the utilization ratio of micro light-emitting diodes.

[0005] In order to solve the above technical problems, a technical proposal adopted by the present disclosure is to provide a pixel structure, the pixel structure includes: a substrate; a black photoresist layer deposited on the substrate, the black photoresist layer including a receiving cavity and an isolation region, and the isolation region is provided in the receiving cavity; a polyelectrolyte layer, the polyelectrolyte layer is coated on the black photoresist layer except the isolation region; a metal nanoparticle layer, the metal nanoparticle layer is covered on the polyelectrolyte layer; a micro light emitting diode, the micro light emitting diode is disposed on the isolation region; wherein the metal nanoparticle including at least one of Au, Ag, Cu, Ni, Co, Pt and/or an alloy formed by at least two of Au, Ag, Cu, Ni, Co and Pt; and the polyelectrolyte layer including at least one of Poly Diallyl Dimethyl Ammonium Chloride, Sodium Polyacrylate, Poly Dimethyl Diallyl Ammonium Chloride, and Acrylic Acid-Vinylpyridine Copolymer.

[0006] In order to solve the above technical problems, another technical solution adopted by the present disclosure is that providing a substrate; forming a black photoresist layer having a receiving cavity and an isolation region on the substrate; coating a polyelectrolyte solution on the surface of the black photoresist layer except the isolation region, and air-dried to form a polyelectrolyte layer; coating a metal nanoparticle solution on the surface of the polyelectrolyte layer, and air-dried to form a metal particle layer; and align and transferring a micro light emitting diodes to the black photoresist layer.

[0007] In order to solve the above-mentioned technical problems, a further technical solution adopted by the present disclosure is to provide a pixel structure, including: a substrate; a black photoresist layer deposited on

the substrate, the black photoresist layer including a receiving cavity and an isolation region, and the isolation region is provided in the receiving cavity; a polyelectrolyte layer, the polyelectrolyte layer is coated on the black photoresist layer except the isolation region; a metal nanoparticle layer, the metal nanoparticle layer is covered on the polyelectrolyte layer; and a micro light emitting diode, the micro light emitting diode is disposed on the isolation region.

[0008] An advantageous effect of the present disclosure is that, comparing to the conventional technology, the present disclosure can improve the light utilization efficiency of the micro light emitting diode by forming a pixel structure having a reflective layer.

BRIEF DESCRIPTION OF THE DRAWINGS

[0009] In order to more clearly illustrate the embodiments of the present application or conventional technology, the following FIG.s will be described in the embodiments are briefly introduced. It is obvious that the drawings are merely some embodiments of the present application, those of ordinary skill in this field can obtain other FIG.s according to these FIG.s without paying the premise.

FIG. 1 is a flow diagram of an embodiment of the method for the fabrication of the pixel structure of the present disclosure; and

FIG. 2 is a schematic structural view of an embodiment of the pixel structure of the present disclosure.

DETAILED DESCRIPTION OF PREFERRED EMBODIMENTS

[0010] Embodiments of the present application are described in detail with the technical matters, structural features, achieved objects, and effects with reference to the accompanying drawings as follows. It is clear that the described embodiments are part of embodiments of the present application, but not all embodiments. Based on the embodiments of the present application, all other embodiments to those of ordinary skill in the premise of no creative efforts acquired should be considered within the scope of protection of the present application.

[0011] Specifically, the terminologies in the embodiments of the present application are merely for describing the purpose of the certain embodiment, but not to limit the invention.

[0012] Referring to FIG. 1, FIG. 1 illustrates the method for the fabrication of the pixel structure of an embodiment of a pixel structure of the present disclosure, the method including the steps of:

S110, preparing a substrate.

[0013] Wherein the substrate can be a transparent material, specifically it can be glass or transparent plastic etc.

[0014] S120, forming a black photoresist layer having

a receiving cavity and an isolation region on the substrate.

[0015] In step S120, the black photoresist layer having the receiving cavity and the isolation region can be formed by adapting photolithography process in one step. Wherein, the black photoresist layer is an organic compound, after being exposed to ultraviolet light, the solubility in the developer will be changed. The photoresist is coated on the surface of the substrate in a liquid state generally, and is baked into a solid state after exposure. The effect is to transfer the pattern on the mask to the oxide layer on the surface of the substrate, so as to protect the following material such as etching or ion implantation, etc.

[0016] The photolithography refers to a process in which a specific portion of the photoresist layer on the surface of the substrate is removed by a series of fabricating steps, after which the photoresist layer with a micro pattern structure is left on the surface of the substrate. By the photolithography process, a feature pattern portion is retained on the surface of the substrate finally. And the three basic elements of the photolithography process is to control the light (mainly ultraviolet light), mask and photoresist.

[0017] In the present embodiment, the black photoresist layer having the receiving cavity and the isolation region can be formed by adapting photolithography process in one step. In other embodiments, the black photoresist layer can also be formed by photolithography in two steps, i.e., a black photoresist layer having a receiving cavity structure is formed first, and the isolation region is further formed in the receiving cavity. Wherein the isolation region can be arranged in a structure such as a projection, a recess or a groove, and the subsequently provided polyelectrolyte solution and the metal nanoparticle solution are not coated on the isolation region, so as to isolate the pins at both ends of the micro light emitting diode, so that the micro light emitting diode is not short. In the present embodiment, the isolation region is provided as a projection structure to facilitate the placement of the micro light emitting diodes.

[0018] S130, the surface of the black photoresist layer except the isolation region is coated with the polyelectrolyte solution, and then be air-dried to form a polyelectrolyte layer.

[0019] In step S130, the coating of the polyelectrolyte layer and the subsequent film layer structure is performed using a layer-by-layer, LBL technique. The layer-by-layer technique is using the method of alternating layers deposition, by the weak interaction function between intermolecular of each of the layers, such as electrostatic attraction, hydrogen bonds, coordination keys, etc., so that the layers and layers are spontaneously associated with the formation process of a complete structure, stable performance, molecular aggregates or supramolecular structure with a specific function. In the present embodiment, the electrostatic layer-by-layer technique is mainly used, such as the electrostatic interaction between ions

is used as the driving force for film formation. The polyelectrolyte layer is formed by air-dried the polyelectrolyte solution coated on the uniformly coated black photoresist layer having the receiving cavity and the isolation region obtained by the above mentioned photolithography process. Wherein, the polyelectrolyte solution can be selected from one of Poly Diallyl Dimethyl Ammonium Chloride, Sodium Polyacrylate, Poly Dimethyl Diallyl Ammonium Chloride, and Acrylic Acid-Vinylpyridine Copolymer. In a specific embodiment, the polyelectrolyte solution is selected from a solution of Poly Dimethyl Diallyl Ammonium Chloride, PDDA at a concentration of 2 mg/ml, and during the coating process, the structure of the isolation region of the black photoresist layer is avoided. After air dried by the air knife, a Poly Dimethyl Diallyl Ammonium Chloride film layer is formed, such is the polyelectrolyte layer.

[0020] S140, coating and air-dried a metal nanoparticle solution on the surface of the polyelectrolyte layer to form a metal particle layer.

[0021] In step S140, a metal nanoparticle solution is further coated on the polyelectrolyte layer. Wherein the metal nanoparticle solution can include at least one of Au, Ag, Cu, Ni, Co, Pt and/or an alloy formed by at least two of Au, Ag, Cu, Ni, Co and Pt. The selected metal nanoparticle solution should have a high extinction coefficient, the so-called extinction coefficient is amount of the absorption of light of the measured solution. In the present embodiment, the Ag nanoparticle solution is selected as the metal nanoparticle solution and coated on the polyelectrolyte layer and air-dried to form an Ag nanoparticle layer. That is, the Ag nanoparticle layer is used as a reflective layer. When the light beam is incident from the air to the surface of the Ag nanoparticle layer, the light amplitude entering the Ag nanoparticle layer is rapidly attenuated, so that the amount of light entering the Ag nanoparticle layer is reduced, while the reflected light energy is increased. And the extinction coefficient of the selected metal particle solution is higher, the more the attenuation of the light amplitude is, the less the light energy entering the metal is, and the higher the reflectance is.

[0022] Besides, in step S130 and step S140, the polyelectrolyte solution and the metal nanoparticle solution are electrically opposite to each other, that is, in step S130, the polyelectrolyte solution is selected a cationic Poly Dimethyl Diallyl Ammonium Chloride, and in step S140, the metal nanoparticle solution is selected the Ag nanoparticles solution with negatively charged. In other embodiments, the polyelectrolyte solution and the metal nanoparticle solution can be selected as long as they meet the opposite electrical properties. And the use of layer-by-layer technique and alternately depositing technique can control the structure and thickness of the assembly film layers. In a specific embodiment, if it is necessary to increase the thickness of the metal nanoparticle layer, the above steps S130 and S140 can be repeated, i.e., one layer of the polyelectrolyte solution is further coated on the metal nanoparticle layer to form a polye-

lectrolyte layer, and a metal nanoparticle solution is coated and air-dried on the polyelectrolyte layer to form one metal nanoparticle layer, so that the desired thickness of film layer can be obtained by repeating the above steps.

[0023] S150, the micro light emitting diodes are aligned and transferred to the black photoresist layer.

[0024] The micro light emitting diodes are aligned and transferred to the black photoresist layer after the formation of the desired thickness of the metal nanoparticle layer by the layer-by-layer technique and alternately depositing technique. Specifically, the micro light emitting diodes are aligned and transferred to the isolation region structure of the black photoresist layer.

[0025] Wherein, the micro light emitting diode is the use of mature light emitting diode manufacturing process to form micro light emitting diode units with the size of 10 ~ 50um grown on the sapphire substrate by the molecular beam epitaxy in large scale. For forming patterned micro light emitting diodes with different color to constitute a display area, the transferring technology through high precision to transfer to the glass substrate is needed. Since the size of the sapphire substrate for fabricating the micro light emitting diodes is essentially the size of the silicon wafers, and the glass substrate with much larger size is used for fabricating the display apparatus, it is necessary to perform multiple transferring, in which the transferring of the micro light emitting diodes is required a special transferring tool, the function of the transferring tool is to align and transfer the micro light emitting diodes from the sapphire substrate to the isolation region of the black photoresist layer, the process can be simply described as: first, contacting the transfer tool with the micro light emitting diode, applying a voltage to the transfer tool and generate a clamping pressure to the micro light emitting diode, picking up the micro light emitting diode with a transfer tool, making the isolation region structure of the black photoresist layer contacting with the micro light emitting diode, and finally releasing the micro light emitting diode to the isolation region structure.

[0026] Further, after the micro light emitting diodes are aligning and transferring to the isolation region structure of the black photoresist layer, due to the optical characteristics of the above-mentioned metal nanoparticle layer, it can make the light emitted from the micro light emitting diodes to the circumferential direction to converge to the light emitting direction again by the refraction and reflection, reduce the light loss and improve the light utilization rate.

[0027] In the above embodiment, it is possible to improve the light utilization efficiency of the micro light emitting diode by using a layer-by-layer technique to form a pixel structure having a reflective layer.

[0028] Referring to FIG. 2, FIG. 2 is a schematic structural view of an embodiment of the pixel structure of the present disclosure. As illustrated in FIG. 2, the pixel structure 10 includes: a substrate 11, a black photoresist layer 12, a polyelectrolyte layer 13, a metal nanoparticle layer 14, and a micro light emitting diode 15.

[0029] Wherein, the substrate 11 can be a transparent material, specifically, glass or transparent plastic or the like.

[0030] The black photoresist layer 12 is deposited on the substrate 11, and the black photoresist layer 12 includes the receiving cavity 121 and the isolation region 122, and the isolation region 122 is provided in the receiving cavity 121. Specifically, the black photoresist 12 is an organic compound, after being exposed to ultraviolet light, the solubility in the developer will be changed. The photoresist is coated on the surface of the substrate in a liquid state generally, and is baked into a solid state after exposure. The effect is to transfer the pattern on the mask to the oxide layer on the surface of the substrate 11, so as to protect the following material such as etching or ion implantation, etc. the black photoresist layer 12 can be formed by adapting photolithography process in one step, it can also be formed by photolithography in two steps, i.e., the black photoresist layer 12 having a receiving cavity 121 structure is formed first, and the isolation region 122 is further formed in the receiving cavity 121. Wherein the isolation region 122 can be arranged in a structure such as a projection, a recess or a groove, and the subsequently provided polyelectrolyte solution and the metal nanoparticle solution are not coated on the isolation region 122, so as to isolate the pins at both ends of the micro light emitting diode 15, so that the micro light emitting diode 15 is not short. In the present embodiment, the isolation region 122 is provided as a projection structure to facilitate the placement of the micro light emitting diodes 15.

[0031] The polyelectrolyte layer 13 is coated on the black photoresist layer 12 except the isolation region 122. Wherein, the polyelectrolyte layer 13 can be one of the Poly Diallyl Dimethyl Ammonium Chloride, Sodium Polyacrylate, Poly Dimethyl Diallyl Ammonium Chloride, and Acrylic Acid-Vinylpyridine Copolymer. In a specific embodiment, the polyelectrolyte layer 13 is selected of Poly Dimethyl Diallyl Ammonium Chloride.

[0032] The metal nanoparticle layer 14 is covered on the polyelectrolyte layer 13, the metal nanoparticle includes at least one of Au, Ag, Cu, Ni, Co, Pt and/or an alloy formed by at least two of Au, Ag, Cu, Ni, Co and Pt. The selected metal nanoparticle layer should have a high extinction coefficient. In the present embodiment, the Ag nanoparticle layer is selected as the reflective layer. When the light beam is incident from the air to the surface of the Ag nanoparticle layer, the light amplitude entering the Ag nanoparticle layer is rapidly attenuated, so that the amount of light entering the Ag nanoparticle layer is reduced, while the reflected light energy is increased. And the extinction coefficient of the selected metal particle solution is higher, the more the attenuation of the light amplitude is, the less the light energy entering the metal is, and the higher the reflectance is.

[0033] In the specific embodiment, the polyelectrolyte layer 13 and the metal nanoparticle layer 14 are electrically opposite to each other, that is, the Poly Dimethyl

Diallyl Ammonium Chloride thin film layer carries positive charge, the Ag nanoparticle layer carries negatively charge. The two layers are alternately deposited by electrostatic interaction to achieve the desired thickness of the metal nanoparticle film layer. In other embodiments, the polyelectrolyte layer and the metal nanoparticle layer may be selected so long as they satisfy the opposite electrical properties.

[0034] The micro light emitting diodes 15 are disposed on the isolation region 122. Wherein, the micro light emitting diode 15 is the use of mature light emitting diode manufacturing process to form micro light emitting diode units with the size of 10 ~ 50um grown on the sapphire substrate by the molecular beam epitaxy in large scale. And it is disposed on the isolation region 122 of the black photoresist layer 12 by a align and transfer, and the specific fabricating method is described above, and will not be described here. Due to the optical characteristics of the above-mentioned metal nanoparticle layer, it can make the light emitted from the micro light emitting diodes to the circumferential direction to converge to the light emitting direction again by the refraction and reflection, reduce the light loss and improve the light utilization rate.

[0035] In view of the foregoing, it will be readily understood by those skilled in the art that the present disclosure provides a pixel structure and a method for fabricating therefor can improve the light utilization efficiency of the micro light emitting diode by using a layer-by-layer technique to form a pixel structure having a reflective layer.

[0036] Above are embodiments of the present application, which does not limit the scope of the present application. Any modifications, equivalent replacements or improvements within the spirit and principles of the embodiment described above should be covered by the protected scope of the invention.

Claims

1. A pixel structure, comprising:

a substrate;
 a black photoresist layer deposited on the substrate, the black photoresist layer comprising a receiving cavity and an isolation region, and the isolation region is provided in the receiving cavity;
 a polyelectrolyte layer, the polyelectrolyte layer is coated on the black photoresist layer except the isolation region;
 a metal nanoparticle layer, the metal nanoparticle layer is covered on the polyelectrolyte layer;
 a micro light emitting diode, the micro light emitting diode is disposed on the isolation region;
 wherein the metal nanoparticle comprising at least one of Au, Ag, Cu, Ni, Co, Pt and/or an alloy formed by at least two of Au, Ag, Cu, Ni, Co and Pt; and

the polyelectrolyte layer comprising at least one of Poly Diallyl Dimethyl Ammonium Chloride, Sodium Polyacrylate, Poly Dimethyl Diallyl Ammonium Chloride, and Acrylic Acid-Vinylpyridine Copolymer.

2. The pixel structure, according to claim 1, wherein the polyelectrolyte layer and the metal nanoparticle layer are electrically opposite to each other.

3. A method for fabricating a pixel structure for improving the utilization ratio of micro light emitting diodes, comprising:

providing a substrate;
 forming a black photoresist layer having a receiving cavity and an isolation region on the substrate;
 coating a polyelectrolyte solution on the surface of the black photoresist layer except the isolation region, and air-dried to form a polyelectrolyte layer;
 coating a metal nanoparticle solution on the surface of the polyelectrolyte layer, and air-dried to form a metal particle layer; and
 align and transferring a micro light emitting diodes to the black photoresist layer.

4. The method for fabricating the pixel structure according to claim 3, wherein the black photoresist layer is formed by adapting photolithography process in one step.

5. The method for fabricating the pixel structure according to claim 3, wherein the black photoresist layer is formed by adapting photolithography process in two step.

6. The method for fabricating the pixel structure according to claim 3, wherein the polyelectrolyte layer and the metal nanoparticle layer are electrically opposite to each other.

7. The method for fabricating the pixel structure according to claim 3, wherein the metal nanoparticle comprising at least one of Au, Ag, Cu, Ni, Co, Pt and/or an alloy formed by at least two of Au, Ag, Cu, Ni, Co and Pt.

8. The method for fabricating the pixel structure according to claim 3, wherein the polyelectrolyte layer comprising at least one of Poly Diallyl Dimethyl Ammonium Chloride, Sodium Polyacrylate, Poly Dimethyl Diallyl Ammonium Chloride, and Acrylic Acid-Vinylpyridine Copolymer.

9. A pixel structure, comprising:

a substrate;

a black photoresist layer deposited on the substrate, the black photoresist layer comprising a receiving cavity and an isolation region, and the isolation region is provided in the receiving cavity;

a polyelectrolyte layer, the polyelectrolyte layer is coated on the black photoresist layer except the isolation region;

a metal nanoparticle layer, the metal nanoparticle layer is covered on the polyelectrolyte layer; and

a micro light emitting diode, the micro light emitting diode is disposed on the isolation region.

10. The pixel structure, according to claim 9, wherein the polyelectrolyte layer and the metal nanoparticle layer are electrically opposite to each other.

11. The pixel structure, according to claim 9, wherein the metal nanoparticle comprising at least one of Au, Ag, Cu, Ni, Co, Pt and/or an alloy formed by at least two of Au, Ag, Cu, Ni, Co and Pt.

12. The pixel structure, according to claim 9, wherein the polyelectrolyte layer comprising at least one of Poly Diallyl Dimethyl Ammonium Chloride, Sodium Polyacrylate, Poly Dimethyl Diallyl Ammonium Chloride, and Acrylic Acid-Vinylpyridine Copolymer.

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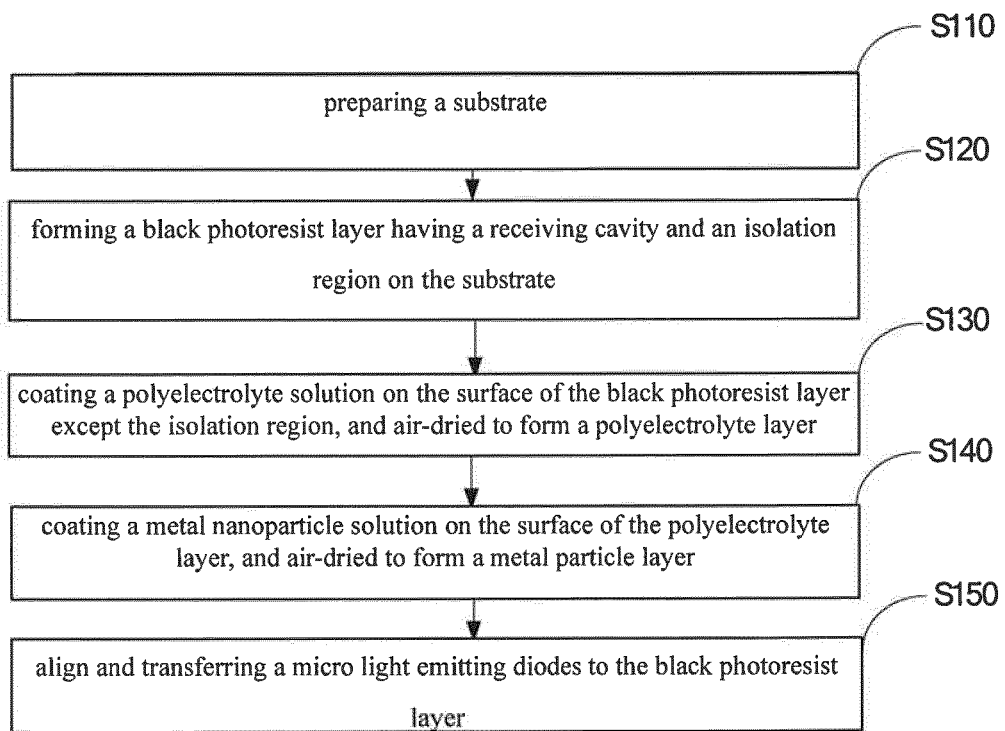


FIG. 1

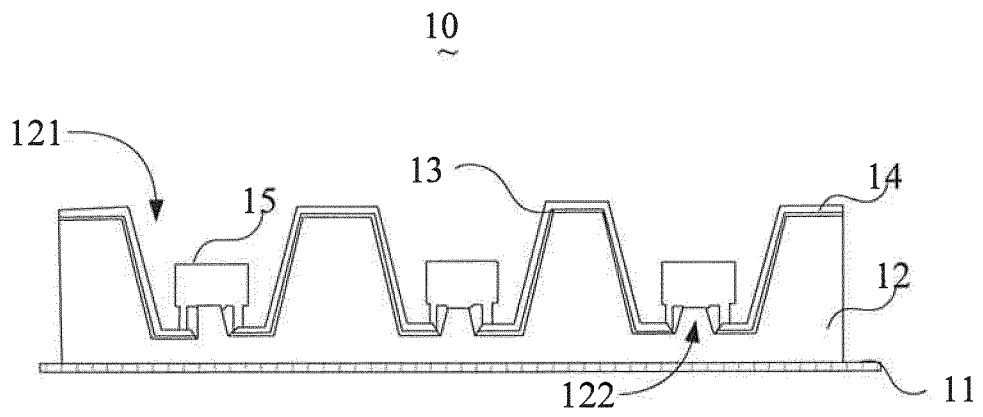


FIG. 2

INTERNATIONAL SEARCH REPORT

International application No.

PCT/CN2017/083689

A. CLASSIFICATION OF SUBJECT MATTER		
H01L 33/00 (2010.01) i; H01L 27/15 (2006.01) i According to International Patent Classification (IPC) or to both national classification and IPC		
B. FIELDS SEARCHED		
Minimum documentation searched (classification system followed by classification symbols) H01L		
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched		
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) CNABS; DWPI: 黑色, 微发光二极管, 引脚, 腔, 凹, 像素, 微型发光二极管, 微型 LED, 微 LED, pixel, microled, cave, pin, opening, recess, groove, MLED, black		
C. DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	CN 105976725 A (SHENZHEN CHINA STAR OPTOELECTRONICS TECHNOLOGY CO., LTD.), 28 September 2016 (28.09.2016), description, paragraphs [0029]-[0034], and figures 2-4	1-12
A	CN 101866075 A (SHANTOU GOWORLD DISPLAY (PLANT II) CO., LTD.), 20 October 2010 (20.10.2010), entire document	1-12
<input type="checkbox"/> Further documents are listed in the continuation of Box C. <input checked="" type="checkbox"/> See patent family annex.		
* Special categories of cited documents: “T” later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention “A” document defining the general state of the art which is not considered to be of particular relevance “E” earlier application or patent but published on or after the international filing date “X” document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone “L” document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) “Y” document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art “O” document referring to an oral disclosure, use, exhibition or other means “&” document member of the same patent family “P” document published prior to the international filing date but later than the priority date claimed		
Date of the actual completion of the international search 27 December 2017		Date of mailing of the international search report 05 January 2018
Name and mailing address of the ISA State Intellectual Property Office of the P. R. China No. 6, Xitucheng Road, Jimenqiao Haidian District, Beijing 100088, China Facsimile No. (86-10) 62019451		Authorized officer DOU, Mingsheng Telephone No. (86-10) 62411819

Form PCT/ISA/210 (second sheet) (July 2009)

INTERNATIONAL SEARCH REPORT
 Information on patent family members

International application No. PCT/CN2017/083689
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Patent Documents referred in the Report	Publication Date	Patent Family	Publication Date
CN 105976725 A	28 September 2016	None	
CN 101866075 A	20 October 2010	None	

Form PCT/ISA/210 (patent family annex) (July 2009)

专利名称(译)	像素结构及其制造方法		
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申请号	EP2017904056	申请日	2017-05-10
[标]申请(专利权)人(译)	深圳市华星光电技术有限公司		
申请(专利权)人(译)	深圳市中国星光电科技有限公司.		
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[标]发明人	LI DONGZE CHEN LIXUAN		
发明人	LI, DONGZE CHEN, LIXUAN		
IPC分类号	H01L33/00 H01L27/15		
CPC分类号	H01L27/156 H01L33/0095 H01L25/0753 H01L33/60 H01L2933/0058 H01L33/62 H01L2933/0066		
优先权	201710207848.5 2017-03-31 CN		
外部链接	Espacenet		

摘要(译)

公开了一种像素结构及其制造方法。该制造方法包括：准备基板（11）；以及将基板（11）制成。在基板上制造具有容纳腔（121）和隔离区（122）的黑色光刻胶层（12）；用聚电解质溶液涂覆黑色光致抗蚀剂层的除隔离区域以外的表面，空气干燥该表面，并形成聚电解质层（13）；用金属纳米粒子溶液涂覆聚电解质层的表面，空气干燥该表面，并形成金属粒子层（14）；将微发光二极管（15）对准并转移到黑色光刻胶层上。通过该方法，可以提高微发光二极管的光利用率。

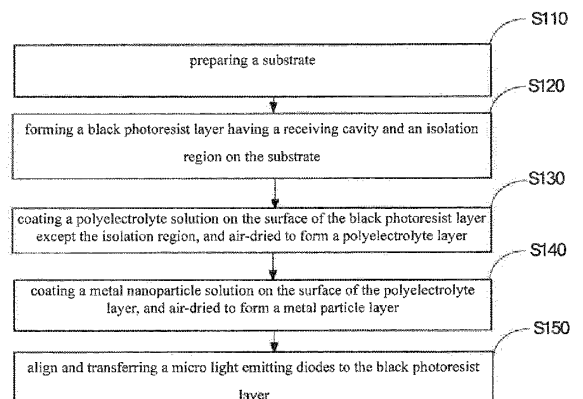


FIG. 1